AMENDMENTS TO THE CLAIMS

Claims 1-5 (Canceled).

- 6. (Currently Amended) An inductor formed on an integrated circuit chip, the inductor comprising:
 - two or more at least two outer layers each including a first portion and a second portion; one or more inner layers at least one inner layer located between said two or more at least two outer layers;
 - inductor metal winding turns included in said one or more at least one inner layers layer; and a series of magnetic metallic strips disposed on in each of said first and second portions of said at least two or more outer layers and on each of said at least one or more inner layers layer, said series of magnetic metallic strips on of said first portion and said second portion in registration with one another and arranged so as to form a particular pattern with one another.
- 7. (Original) An inductor according to claim 6, wherein said particular pattern is a perfect orthogonal grid pattern.
- 8. (Original) An inductor according to claim 6, wherein said particular pattern is a transverse grid pattern.
- 9. (Original) An inductor according to claim 6, wherein said series of magnetic metallic strips substantially surrounds said inductor metal winding turns.
- 10. (Original) An inductor according to claim 6, further comprising gaps between each of said two or more outer layers and said one or more inner layers.

Claims 11-22 (Canceled).

- 23. (New) An inductor, comprising:
 - a plurality of layers comprising, in the following order relative to one another:
 - a first outer core layer including a first region containing a first series of magnetic core strips aligned with one another;

- a first intermediate core layer including a second region containing a second series of magnetic core strips; said second region in registration with said first region and said second series of magnetic core strips aligned with one another and non-parallel relative to said first series of magnetic core strips;
- a first winding/core layer including a third region containing metal winding turns and a third series of magnetic core strips, said third region in registration with said second region;
- a second intermediate core layer including a fourth region containing fourth series of magnetic core strips aligned with one another, said fourth region in registration with said third region; and
- a second outer core layer including a fifth region containing a fifth series of magnetic core strips in registration with said first series of magnetic core strips; said fifth region in registration with said fourth region and said fifth series of magnetic core strips aligned with one another and non-parallel relative to said fourth series of magnetic core strips.
- 24. (New) An inductor according to claim 23, further comprising a first gap located between said first intermediate core layer and said first winding/core layer and a second gap located between said first winding/core layer and said second intermediate core layer.
- 25. (New) An inductor according to claim 23, wherein said first series of magnetic core strips and said second series of magnetic core strips form an orthogonal grid relative to one another.
- 26. (New) An inductor according to claim 25, wherein said fourth series of magnetic core strips and said fifth series of magnetic core strips form an orthogonal grid relative to one another.

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